

**INFORMATION DISCLOSURE CITATION**  
(Use several sheets if necessary)

Docket Number (Optional)

END 9/19/98 11054

Application Number

Applicant(s)

Jimarez et al.

Filing Date

Group Art Unit

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		4,376,505	3/1983	Wojcik			
		5,060,844	10/1991	Behun et al.			
		5,147,084	9/1992	Behun et al.			
		5,616,520	4/1997	Nishiuma et al.			
		5,641,113	6/1997	Somaki et al.			
		5,656,862	8/1997	Papathomas et al.			
		5,751,068	5/1998	McMahon et al.			
		5,773,889	6/1998	Love et al.			
		5,611,481	3/1997	AKamatsuet al.			
		6,213,347	4/1999	Thomas			

**FOREIGN PATENT DOCUMENTS**

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
	08-17860	1/96	JP				
	06-177205	6/94	JP				
	10-098045	4/98	JP				
	11-168116	6/99	JP				

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

	IBM Technical Disclosure Bulletin, Vol. 32, No. 10B, March 1990, ENCAPSULATED SOLDER JOINT FOR CHIP MOUNTING, page 480.
	IBM Technical Disclosure Bulletin, Vol. 32, No. 6A, November 1989, IMPROVED C4 RELIABILITY USING LOW MODULUS DIELECTRIC LAYER, pages 315-316.

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2ND 0919980110 US4

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INITIAL

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Topper et al., WAFER LEVEL PACKAGE USING DOUBLE BALLS, 2000 International Symposium on Advanced Packaging Materials, pages 198-200.

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**FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	10-303245	11/98	JP				
	10-107081	4/98	JP				
	06-350250	12/94	JP				
	06-61303	3/94	JP				
	11-330126	11/99	JP				

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)


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